## What is claimed is:

1	<u>,</u> 1.	A method comprising:		
2	pro	oviding a capacitor to maintain a terminal voltage of a pixel cell near a		
3	predetermined voltage;			
4	pro	oviding a memory to store a digital indication of the predetermined voltage;		
5	and			
6	du	ring a refresh operation, converting the digital indication into an analog voltage		
7	to update a charge on the capacitor.			
1	2.	The method of claim 1, wherein the memory comprises a static random		
2	access memory.			
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1	3.	The method of claim 1, further comprising:		
2	du	ring the refresh operation, reading the digital indication from the memory.		
1	4.	The method of claim 1, further comprising:		
2	du	ring the refresh operation, latching the digital indication.		
1	5.	The method of claim 1, further comprising:		
2	up	dating the memory with another digital indication of another predetermined		
3	voltage.			

1	<b>.</b> 6.	A method comprising:	
2	rovid	ling capacitors, each capacitor being associated with a different pixel cell to	
3	maintain a ter	minal voltage of the associated pixel cell near a predetermined voltage;	
4	provid	ling memory buffers, each memory buffer being associated with a different	
5	one of the pix	el cells and storing a digital indication of the associated predetermined	
6	voltage;		
7	during	g a refresh operation, converting the digital indications into analog voltages	
8	to update char	rges on the capacitors.	
1	7.	The method of claim 6, wherein the capacitors are associated with a row	
2	of pixels.		
1	8.	The method of claim 6, wherein the memory buffers comprise a part of a	
2	static random	access memory.	
1	9.	The method of claim of further comprising:	
2	during	g the refresh operation, reading the digital indications from the memory	
3	buffers.		
1	10.	The method of claim 6, further comprising:	
2	during	g the refresh operation, latching the digital indications.	
1		An light modulator call comprising:	
1	N.	An light modulator cell comprising:	
2	a pixe	icitor to maintain a terminal voltage of the pixel cell near a predetermined	
3	-	chei to maintain a terminar voltage of the pixer cen near a predetermined	
4 5	voltage;	nory to store a digital indication of the predetermined voltage; and	
6	a digital-to-analog converter to convert the digital indication into an analog		
7	_	date a charge on the capacitor during a refresh operation.	
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	1	12. The light modulator cell of claim 11, wherein the memory comprises a
	2	static random access memory.
	1	13. The light modulator cell of claim 11, further comprising:
	2	bit latches; and
	3	sense amplifiers to communicate the digital indication from the memory to the bit
	4	latches during the refresh operation.
	1	14. The light modulator cell of claim 10, further comprising:
41	2	bit latches to latch the digital indication during the refresh operation.
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14. Turk	1	15. The light modulator cell of claim 11, wherein the memory further is
# # # # # # # # # # # # # # # # # # #	2	updated with another digital indication of another predetermined voltage.
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	1	16. A light modulator comprising:
	2	pixel cells;
<u>.</u>	3	capacitors, each dapacitor being associated with a different pixel cell to maintain
	4	a terminal voltage of the associated pixel cell near a predetermined voltage;
4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	5	memory buffers, each memory buffer being associated with a different one of the
	6	pixel cells and storing a digital indication of the associated predetermined voltage; and
	7	digital-to-analog converters to convert the digital indications into analog voltages
	8	to update charges on the capacitors during a refresh operation.
	1	17. The light modulator of claim 16, wherein the capacitors are associated
	2	with a row of pixels.
	1	18. The light modulator of claim 16, wherein at least one of the memory
	2	buffers comprises a static random access memory.
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